PATENT NO.

: 7,050,343 B2

Page 1 of 4

APPLICATION NO.: 10/789443

DATED

: May 23, 2006

INVENTOR(S)

: Promod Kumar et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Delete: "failures"

Column 1, Line 67

Insert: -- failure --

Delete: "to the"

Column 2, Line 32

Insert: -- on the --

Delete: "relatives"

Column 4, Line 59

Insert: -- relative --

Delete: "register"

Column 4, Line 62

Insert: -- registers --

Delete: "in which preserve"

Column 4, Lines

Insert: -- which preserves --

66-67

Delete: "at the time"

Column 5, Line 50

Insert: -- at a time --

Delete: "contains"

Column 7, Line 14

Insert: -- contained --

Delete: "occurs"

Column 7, Line 16

Insert: -- occurred --

Delete: "of the all"

Column 7, Line 44

Insert: -- of all --

Delete: "are"

Column 7, Line 55

Insert: -- is --

Delete: "identify"

Column 7, Line 62

Insert: -- identifying --

Delete: "occupy"

Column 8, Line 12

Insert: -- occupying --

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Page 2 of 4

Delete: "two side"

Column 8, Line 21

Insert: -- two sides --

Delete: "to the"

Column 9, Line 18

Insert: -- than the --

Delete: "column"

Column 9, Line 39

Insert: -- columns --

Delete: "erase"

Column 10, Line 14

Insert: -- Erase --

Delete: "read"

Column 10, Line 15

Insert: -- Read --

Delete: "setting"

Column 10, Line 20

Insert: -- set --

Delete: "at beginning"

Column 11, Line 13

Insert: -- at the beginning --

Delete "electronic"

Column 11, Line 15

Insert: -- Electronic --

Delete: "at the time"

Column 11, Line 22

Insert: -- at a time --

Delete: "parametric measurement"

Column 11, Lines

Insert: -- Parametric Measurement --

33-34

Delete: "erase the step"

Column 12, Line 34

Insert: -- the erase step --

Delete: "perform"

Column 12, Line 60

Insert: -- performs --

Delete: "correspond"

Column 13, Line 18

Insert: -- corresponds --

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: Promod Kumar et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Delete: "catch"

Column 13, Line 22

Insert: -- Cache --

Delete: "vgmin/vgmax"

Column 13, Line 64

Insert: --Vgmin/Vgmax--

Delete: "vgmax/vgmin"

Column 14, Line 2

Insert: -- Vgmax/Vgmin --

Delete: "vgmax/vgmin"

Column 14, Line 26

Insert: -- Vgmax/Vgmin --

Delete: "vgmax/vgmin"

Column 14, Line 28

Insert: -- Vgmax/Vgmin --

Delete: "for vgmax"

Column 14, Line 30

Insert -- for Vgmax --

Delete: "for vgmin"

Column 14, Line 31

Insert: --for Vgmin --

Delete: "vgmax or vgmin"

Column 14, Line 45

Insert: -- vgmax or Vgmin --

Delete: "vgmax"

Column 14, Lines

Insert: -- Vgmax --

65 and 66

Delete: "vgmin"

Insert: -- Vgmin --

Delete: "all0/all1"

Column 15, Line 16

Insert: -- AllO/All1 --

Delete: "standard" Insert: -- invention --

Column 16, Line

(second column)

Delete: "ck, ckn, all0/all1"

Column 16, Line 26

Insert: -- CK, CKN, All0/Aill1 --

Delete: "ckn"

Column 16, Line 28

Insert: -- CKN --

PATENT NO.

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APPLICATION NO.: 10/789443

DATED

: May 23, 2006

INVENTOR(S)

: Promod Kumar et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Delete: "ck"

Column 16, Lines

Insert: -- CK --

29 and 30

Delete: "Ck/Ckn"

Column 16, Line 33

Insert: -- CK/CKN --

Delete: "all0/all1"

Column 16, Line 42

Insert: -- Allo/Ail1 --

Delete: "both"

Column 16, Line 60

Insert: -- by--

Delete: "numerosity"

Column 17, Line 13

Insert: -- number--

Delete: "an built-in"

Column 17, Line 23

Insert: -- a built-in --

Delete: "of the time"

Column 17, Line 42

Insert: -- as the time --

Delete: "microcontroller"

Column 19, Line 47

Insert: -- micro-controller --

Column 20, Line 46

Signed and Sealed this

Fifth Day of December, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office